TECHNIQUES FOR MULTI-CORE DEBUGGING

Abstract

Methods, systems, and apparatus are provided for multi-core debugging. A single processor having multiple processing cores dynamically and simultaneously establishes multiple debugging sessions on different ones of the processing cores. Existing applications not associated with the debugging sessions are maintained and processed without interruption and existing states associated with the processor and the processing cores are maintained before and after the debugging sessions are established.

"Express Mail" mailing label number: <u>EV 370239705 US</u> Date of Deposit: <u>March 30, 2004</u>

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.